DKTE Society's

TEXTILE AND ENGINEERING INSTITUTE

Rajwada, Ichalkaranji (An Autonomous Institute)

AICTE Funded One Week FDP On

Digital Signal Processing Algorithms:
Optimization and Implementation

(2nd December to 7th December 2019)

REGISTRATION FORM

Name :
Designation :
Qualification :
Experience (in years) :
a) Teaching :
b) Research:
c) Industry :
Accommodation Required: Yes/No
Institution / Organization :
Address for communication :
Mobile/ Tel No. :
E-mail :
Online Transaction No of Refundable Caution

Money deposit of Rs. 1000/-:

Signature of Head of Institution (With seal)

Signature of the Applicant



Patron

Mr. Kallappanna Baburao Awade

Chairman, Board of Governance, DKTE Society, Ichalkaranji

Convener

Prof. (Dr.) P. V. Kadole

Director, TEI, Ichalkaranji

Program Coordinator

Prof. (Dr.) S. A. Patil

Professor and Head,

Department of Electronics & Telecommunication Engineering, TEI Ichalkaranji

Organizing Committee Members

Prof. A. D. Kadage, Asst. Professor, TEI, Ichalkaranji

Prof. S. R. Prasad, Asst. Professor, TEI, Ichalkaranji

Prof. V. B. Sutar, Asst. Professor, TEI, Ichalkaranji

Prof. P. C. Dhanawade, Asst. Professor, TEI, Ichalkaranji

Prof. A. R. Telepatil, Asst. Professor, TEI, Ichalkaranji

Prof. V. M. Davande, Asst. Professor, TEI, Ichalkaranji

CONTACT PERSONS

Co-ordinator:

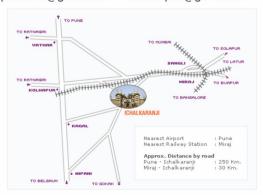
Prof. (Dr.) S. A. Patil 09822524667 E-mail: shrinivasapatil@gmail.com

...Co-coordinators...

Prof. S. R. Prasad Prof. A. D. Kadage 09423285079 9011395972

saurabhprasad21@gmail.com awatidipali@gmail.com









DKTE Society's TEXTILE AND ENGINEERING INSTITUTE

Rajwada, Ichalkaranji (An Autonomous Institute)

One Week

Faculty Development Program on

Digital Signal Processing Algorithms:

Optimization and Implementation

(2nd December to 7th December 2019)

Funded by
All India Council of Technical Education

Organized by

Dept. of Electronics & Telecommunication

Textile and Engineering Institute,

Ichalkaranji - 416 115 (MS)



ABOUT INSTITUTE:

DKTE Society's Textile and Engineering Institute Ichalkaranji was established in 1982. Presently the institute conducts various UG, PG, and PhD degree courses in Textiles, Engineering, and Management. The total student strength of the institute is around 3700 and has 200+ teaching faculty members and around 160 non-teaching staff. The institute is equipped with excellent infrastructural facilities and a highly qualified staff. Apart from regular teaching, the institute has interactions with renowned industries. As a result of this, a high standard of education is maintained. The institute is affiliated to Shivaji University, Kolhapur, and has got autonomous status. It has been ranked A+ by NAAC with 3.53 CGPA and most of the courses are accredited by NBA.

ABOUT DEPARTMENT:

The department has been established in the year 2002-03, and twice it has been crowned with NBA accreditation. The department has highly qualified and experienced faculties along with state-of-art laboratories in the areas like Digital Electronics, Communication Engineering, Digital Signal Processing, Embedded System, VLSI Design, Power Electronics and Computer Networks. The department always strives to impart best expertise knowledge to students so that they can tackle engineering problems with immense confidence.

OBJECTIVE OF THE FDP:

Digital Signal Processing is an ever growing field and finds huge applications in the field like communication, biomedical signal processing, imaging, automation, surveillance; to name a few. In most of the Universities, it is also a core professional course in the Electronics/ E&TC branch. This FDP aims to provide fundamental as well as advanced knowledge in the field of signal processing and provide hands on session on implementation of DSP algorithms using some of the hardware like DSP Processors, FPGA, Raspberry Pi, ARM cortex etc.

PROGRAM CONTENT:

- Implementation of Signal processing Algorithms
 Using Raspberry Pi
- Implementation of Signal processing Algorithms
 Using ARM cortex
- Adaptive Filter Design
- Wavelet Transform
- Design of FIR and IIR filter
- Multirate Systems and Filter Bank
- Fourier Transform
- DSP on FPGA boards demo
- Digital Signal Processing for Wireless Application
- Speech Signal Processing
- Two band perfect reconstruction filter bank
- Digital Signal Processing for Smart Antenna
- Hardware implementation of DSP algorithms on DSP Processors using CCS
- Hands on sessions

Eminent Resource Persons:

Dr. Vishwas Udpikar, Sci-com Software, Pune

Dr. M. S. Sutaone, COEP Pune

Dr. V. S. Vyas, COEP Pune

Dr. S. V. Bonde, SGGS, Nanded

Dr. Priyatamkumar, KLE Tech., Hubbali

Dr. B. G. Nagraja, JIT, Davangere

Dr. B. B. Godbole, KBP COE, Satara

Dr. D. S. Aladar, KBP COE, Satara

Mr. Pradip Thorbole, CoreEL Technologies, Bangalore

DATES TO REMEMBER:

Last date of application: November 15, 2019 Confirmation of selection: November 20, 2019

ELIGIBILITY:

Faculties from AICTE recognized colleges and institutes

TARGET GROUP:

This FDP is designed to refresh and motivate faculty members from Engineering colleges and polytechnics who are involved in teaching or research in the field of Signal Processing.

REGISTRATION:

- 1) There are no any registration fees
- 2) However applicants have to deposit an amount of Rs 1000/- as caution money which will be refunded after successful completion of the course.
- 3) The docket with learning resources will be provided to all the registered participants.
- 4) Travelling allowance will be provided to eligible participants as per AICTE guidelines.
- 5) Number of participants is limited to 40 only

Account Details to Deposit Caution Money:

Account Name:

DKTE's Textile & Engineering Institutes

BANK NAME: Axis Bank Ltd

IFSC Code: UTIB 0000606

MICR NUMBER: 416211202

ACCOUNT No: 606010100009140

SWIF CODE: AXIS INBB-134

Branch Code: 606

How to Apply:

FDP Brochure and Registration form can be downloaded from institute's website. The Institutions/ participants may email the filled Registration form and sponsorship certificate along with proof of payment of caution money deposit (Online payment receipt) before due dates on coordinator's or co-coordinator's email id.

BOARDING & LODGING:

High Tea and working lunch will be provided to all the participants. Accommodation may be provided to limited number of outstation participants on their prior request.